

Fast-Track Review of Proposals Meeting 1

Attendees

Bob Myers
Erich Marschner
Chuck Swart
Peter Ashenden
Tim Schneider
David Bishop
Ryan Hinton
John Ries
Stephen Bailey

Proposals which are identified as “finalized” are ready to be put to an approval vote of the WG. Proposals that were rejected or otherwise not acted upon will not be put to a vote. Depending on the recommendation, non-actioned proposals may be considered for a future revision.

FT-xx proposals can be found here: <http://www.eda.org/vhdl-200x/vhdl-200x-ft/proposals/proposals.html>

CP-xxx proposals merged from the 1164 group can be found here:
http://www.eda.org/vhdl-200x/vhdl-200x-ft/proposals/std_logic_1164/proposals.html.

Proposals Reviewed that Are Finalized

- CP-001 (from 1164). Done.
- CP-002 (from 1164). Done. Note that Erich requested a style-only “cleanup” on the code which can be done in the LRM editing phase.
- CP-003 (from 1164). Done. It was requested that comments be added to the source or the semantic implementation be changed to better reflect the functionality implemented. Again, these are stylistic and not substantive changes which will be taken up in the LRM editing process.
- CP-004 (from 1164). Confirmed that additional consideration of this proposal will be deferred to a future revision.
- CP-005 (from 1164). Rejection of proposal confirmed.
- CP-006 (from 1164). Done. Since FT-2 adds unary operators to VHDL, the function names will be changed from xyz_REDUCE to “xyz” to overload the new unary operators.

NOTED: FT-23 proposal scope should be considered to be broadened to include any handling of 1164 (or 1076.2 and 1076.3) packages that goes beyond lexical inclusion of the package text into 1076. For example, should operations, subprograms, etc. in these packages be considered as implicitly declared?

- CP-008 (from 1164). Rejection of proposal confirmed. The functionality is provided in a different form in CP-007.
- CP-009 (from 1164). Rejection of proposal confirmed. For consistency with resolution of FT-5A and FT-6, CP-007 will include to_string() functions as the realization of the capability requested.
- CP-010 (from 1164). Done. David Bishop will review and change, if needed, the std_match() functions to ensure they handle null arrays consistent with this proposal.
- FT-1. Done. However, Erich suggested a better wording of the proposed LRM language change. The proposal will be updated with the new wording.
- FT-2. Done.

Proposals Requiring Additional Work

- CP-007 (from 1164). Dave will work with Jim Lewis to update the proposal along the following:
 - Functionality remains the same.
 - But std_logic_textio will be an empty package that is provided for backward compatibility purposes (so people do not need to delete the “use IEEE.std_logic_textio.all” clause from existing code).
 - The functionality in std_logic_textio will be moved to std_logic_1164.
 - Since users of std_logic_textio must also use std_logic_1164, the functionality will be visible and existing code will be forward compatible.
 - New VHDL code will only need the use clause for std_logic_1164 as all the textio routines will be provided in the package.
 - This same approach will be taken for any other standard packages for which textio functionality will be provided.